

### Product Characteristics:

#### L1 PHY: Baseband Features

- 4x4 MIMO TDD/FDD support
- Single Cell Bandwidth up to 20 MHz
- 400 Mbps DL and 150 Mbps UL
- Beam forming support
- eICIC support
- Inter and Intra band carrier aggregation

#### L2/L3: Higher layer Features

- Quad-core interAptiv
- Flexible RTOS and SMP Linux
- Security accelerators, trusted boot and secure zone
- Data path acceleration
- Avara Processor for advanced networking and DSP functions

#### Interface support

- DDR3, CPRI, JESD204B, USIM, I2C, UART, GPS and SPI

SCBM3404 is a highly integrated, high performance and cost-effective System on Chip for 4G/LTE base stations. It is designed to provide unmatched cost to performance ratios at extremely low bill of materials for the system.

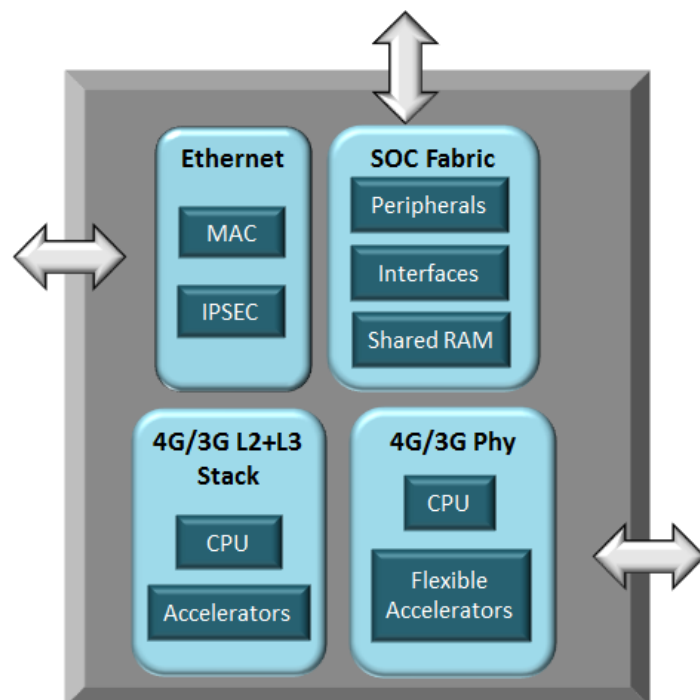
SCBM3404 chip is based on Signalchip's ultra-powerful Encephalon Platform. This platform exploits a unique partition of data path and control functions in complex wireless systems and facilitates extreme performance with compact and power packed soft accelerators.

#### Unparalleled Integration

SCBM3404 integrates a flexible multi-standard modem based on Signalchip's innovative physical layer, powerful quad core MIPS interAptiv processor, backhaul and air interface security subsystems. This is augmented by Signalchip's Avara network processor and a wide variety of system peripherals.

#### Optimized Software Support

The L2/L3 layers are powered by a quad-core MIPS interAptiv processor running flexible RTOS and SMP Linux. Security accelerators for Kasumi, SNOW-3G, ZUC, AES, F8/F9 and support for trusted boot are provided.



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